

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 08/530,661
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Inventor Brent Keeth
Assignee SEF 12 1997 Micron Technology, Inc.
Group Art Unit 2503
Examiner N. Kelly
Attorney's Docket No. MI22-356
Title: Semiconductor Memory Circuit

TRANSMITTAL LETTER AND CERTIFICATE OF MAILING

To: Assistant Commissioner for Patents
Washington, D.C. 20231

From: Wells, St. John, Roberts.
Gregory & Matkin P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3817
Telephone: (509) 624-4276
Fax: (509) 838-3424

Enclosed are:

1. PTO Return Postcard Receipt
2. Transmittal Letter with Certificate of Mailing included.
3. Supplemental Response to March 19, 1997 Office Action.

Small Entity Status is claimed. Large Entity Status Applies.

Deposit Account Authorization - The Commissioner is hereby authorized to charge payment of fees or credit overpayment to Deposit Account No. 23-0925 in connection with: any patent application processing fees under 37 CFR 1.17; and any filing fees under 37 CFR 1.16 for the presentation of extra claims.

Date: 9/17/97

By: *David G. Latwesen*

David G. Latwesen, Ph.D.
Reg. No. 38,533

CERTIFICATE OF MAILING

I hereby certify the items listed above as enclosed are being deposited with the U.S. Postal Service as first class mail in an envelope addressed to The Assistant Commissioner for Patents, Washington, D.C. 20231, on the below-indicated date.

Dated: 9/17/97

Signature: *Jan McMann*
Name: Jan McMann

APPLICANT'S CALCULATION OF TOTAL FEES DUE